

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Teruhiko KAMIGATA, et al.

Serial No. 10/053,707

Group Art Unit: 2154

Confirmation No. 7916

Filed: January 24, 2002

Examiner: Larry D. Donaghue

For: INSTRUCTION PROCESSING METHOD FOR VERIFYING BASIC INSTRUCTION
ARRANGEMENT IN VLIW INSTRUCTION FOR VARIABLE LENGTH VLIW
PROCESSOR

AMENDMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed March 27, 2008, and having a period for response set to expire on June 27, 2008.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.